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MA Holliday - portal.acm.org

... Both lock contention and responsiveness **increase**. ... To determine the effect of **page size** we considered page sizes of 512 bytes and 2 kilobytes for the reference ...

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M Talluri, S Kong, MD Hill, DA Patterson - Proceedings of the 19th annual international symposium on ..., 1992 - portal.acm.org

... Our results show that increasing the **page size** to 32KB causes both a significant **increase** in average working set size (eg, 60%) and a significant reduction in ...

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[Physical address size selection and page size selection in an address translator - all 6 versions »](#)

DB Alpert, KD Shoemaker, KC Kahn, KK Lai - US Patent 5,802,605, 1998 - Google Patents

... Le., greater than 32 bits (eg 36 bits and up), with no **increase** in access ... fields is dependent upon the selected memory size and the 395/569 selected **page size**. ...

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[\[PDF\] Experiments on Page Size, Program Access Patterns, and Virtual Memory Performance - all 2 versions »](#)

DJ Hatfield - IBM Journal of Research and Development, 1972 - research.ibm.com

... also. Of course one would expect the advantage of the small **page size** to **increase** as the amount of space available decreased. ...

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YA Khalidi, MN Nelson, M Talluri, D Williams - 1993 - portal.acm.org

... Otherwise, TLB miss handling may become a performance bottleneck. One way to **increase** the amount of memory mapped by the TLB is to **increase** the **page size**. ...

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RE KESSLER, MD HILL - ACM Transactions on Computer Systems, 1992 - portal.acm.org

... performance. We focus on 1A large cache is defined to be one whose size divided by its associativity exceeds the **page size**. The ...

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[Surpassing the TLB Performance of Superpages with Less Operating System Support - all 8 versions »](#)

M Talluri, MD Hill - portal.acm.org

... **page size** and super- pages of 16KB, 64KB, 256KB, 1MB, 4MB and 16MB. The clear motivation for supporting superpages is that using them appears to **increase** TLB ...

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